

Dr. D. S. Patil



Dr. D. S. Patil, Reader in Electronics

Mail: patildsp@rediffmail.com

Office Phone: +91-257-2257476

Dr. D. S. Patil graduated from Poona University with a rank. He received the M.Sc. degree in Electronics Science with a first class from the Poona University, in 1986, the M.C.M. degree with A+ grade from Poona University, the Ph.D. degree in Electronics from the North Maharashtra University, Jalgaon [Maharashtra], India. He qualified state eligibility test in Electronics in 1995. Since 1991, he has been working in the North Maharashtra University, Jalgaon. He developed new practical for M.Sc. (Electronics) and set up the laboratory as a first member in the Electronics department. He is currently working as a Reader in the Department of Electronics of the North Maharashtra University, Jalgaon. He secured high school, national merit scholarship and received Rashtriya gaurav award sponsored by India International Friendship Society and Young scientist project from Department of Science and Technology, India. His name has been considered in the Steering committee as a member for the International Conference on Nanoscience and Technology, Colorado, United States of America. He worked on the various committees of the universities. He has published about 97 papers in reputed journals and proceedings of the conferences. His research interests include the computer simulation of semiconductor and optoelectronics devices, nano-electronics, Materials development and characterization for the nano-technological and optoelectronics applications, process automation using advanced microcontrollers and embedded systems, organic electronics and computer simulation of nanostructures including quantum dots and superlattice. He has developed with his research student Mr. Edmund Samuel a novel model of probability density spreading in GaN quantum wells. He has developed with master and research students, computer controlled dip coating system for the deposition of advanced materials and microcontroller based spin coating system for the deposition of nano-materials. Presently, he is guiding 05 Ph. D. students and 3 M. Phil students for their innovative research. He has guided about 150 master students for their projects on embedded systems, circuit development and fabrication of advanced materials. He visited France to attend international conference and present his paper. He has delivered many invited talks and popular lectures. He worked as a visiting lecturer to teach advanced courses. He has about 17 years of teaching experience at postgraduate level and about 5 years of experience at graduate level.

List of Research Papers Published

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